

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	§	
Wu et al.	§	Group Art Unit: Unknown
	§	
Serial No. Unknown	§	
	§	Examiner: Unknown
Filed: Herewith	§	
	§	
For: Low Power Fuse Structure and	§	
Method For Making The Same		

INFORMATION DISCLOSURE STATEMENT

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 CFR §1.56, and in accordance with the practice under 37 CFR §1.97 and §1.98, the Examiner's attention is directed to the documents listed on the enclosed modified Form PTO-1449. No inference should be made that the cited references are in fact material, are in fact prior art, or that no better art exists. The cited patents are listed in numerical order and are not in any order based on their pertinence.

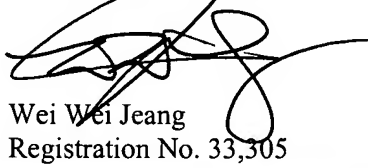
The above-identified application is being filed after June 30, 2003. Therefore, pursuant to the waiver of the requirement under 37 CFR 1.98 (a)(2)(i) as stated in a Pre-OG Notice dated July 11, 2003, copies of only the foreign patent documents and non-patent literature listed on the enclosed modified Form PTO-1449 are attached.

This Information Disclosure Statement is being filed within three months of the United States filing date or before the mailing date of a first Office Action on the merits. No certification or fee is required (37 CFR §1.97(b)).

The Commissioner is hereby authorized to charge any additional fees which may be required or credit any overpayment to Deposit Account 08-1394.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Respectfully submitted,



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Registration No. 33,305

Date: March 22, 2004
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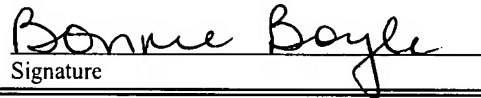
R-70197.1

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 3-22-04.

Bonnie Boyle

Printed Name



Signature

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	Unknown
Filing Date	Herewith
Applicant(s)	Wu et al.
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	24061.200 (TSMC2003-1585)

SHEET	1	OF	1
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER PRIOR ART

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS); title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published
	AG	ALAVI, et al., <u>A PROM Element Based on Salicide Agglomeration of Poly Fuses in a CMOS Logic Process</u> , December 1997, 4 pgs., IEEE International Electron Device Meeting.
	AH	MICHAEL WU, <u>Electrical Fuse Structure for Low Voltage and Single Core Applications</u> , December 16, 2003, 4 pgs., Taiwan Semiconductor Manufacturing Company, Ltd.

Examiner
Signature

Date
Considered